

WHAT THE INVENTION CLAIMED IS

1. A linking method under a mother and child block architecture for building a check area and a logic page of a child block in a flash memory, wherein when a host writes data into a logic block of said flash memory, the writing method comprising:

5 defining a block corresponding to said logic block as a mother block;

 locating a new block from a backup block and defining said new block as a child block, wherein said mother block and said child block are directed at said logic block;

 recording the data into a page of said child block;

 using a redundant page in said block for creating a check area;

10 recording said page of said child block belonging to a page in said mother block;

and

 using said check area consisting a logic page for identifying whether the data retrieved is from said mother block or child block in the subsequent time.

2. The linking method under a mother and a child block architecture for building a
15 check area and a logic page of a child block according to claim 1, wherein when a host is ready for reading said page in said logic page, said child block corresponding to said mother block in said logic page is read and the remaining unmarked pages in said child block are read all from said pages of said mother block.

3. The linking method under a mother and a child block architecture for building a
20 check area and a logic page of a child block according to claim 1, wherein said redundant page in said page of child block is defined as three bytes, wherein a first and a second byte represent as pages of said child block and a third byte represents a page of said mother block.

4. The linking method under a mother and a child block architecture for building a check area and a logic page of a child block according to claim 1, wherein when said host repeats writing data into said logic area, the data continuously written into said page of said child block and when said page of child block is full, a new block is located for moving a valid block of said mother block and said child block into therein and then said mother block and child block are erased.

5. A linking method under a mother and a child block architecture for building a check area and a logic page of a child block in a flash memory, wherein when a host writes data into a logic block of said flash memory, the writing method comprising:

- (a) defining a actual page corresponding to a logic page as a mother block;
- (b) locating a new block and defining it as a child block;
- (c) writing data into said page of said child block; and
- (d) marking a redundant area of said page in said child block belonging to a page of said mother block.